



INDIAN INSTITUTE OF INFORMATION TECHNOLOGY KOTTAYAM
JOINTLY WITH C-DAC TRIVANDRUM ORGANIZES

2-DAY HANDS-ON WORKSHOP ON DIR-V VEGA PROCESSORS AND ECOSYSTEM



India's Indigenous Processor Developed by C-DAC

Patrons

Dr. Rajiv Dharaskar, Director
Dr. M Radhakrishnan, Registrar
Prof. P Mohanan, PIC Academics

Organizers

Dr. Kala S
Assistant Professor, ECE
Head, Integrated Circuits &
Systems Research Group - IIITK

Dr. Minu A Pillai
Assistant Professor, ECE
Integrated Circuits & Systems
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About Workshop

This National Workshop on indigenous RISC-V based VEGA processors will provide an insight to the VEGA series of processors and ecosystem focusing on both theoretical and practical aspects of the processors, development boards, SDK, and applications. Hands-on sessions will be conducted on ARIES boards which are compatible with Arduino. **Certificates** will be issued after the workshop. Sessions are conducted by experts from C-DAC. **Hardware Challenge** based on VEGA processor will also be conducted and attractive prizes will be given to winners !

Registration Details

Registration is mandatory.
Registration Deadline: 27 Jan 2024
Eligibility: All interested candidates
Registration fee: Rs. 500/-
(Includes registration kit, lunch, tea & snacks)
Fill the Registration form after fee payment.
Fee Payment link:
<https://www.onlinesbi.sbi/sbicollect/icollecthome.htm>
Steps for payment:
1. Choose category as Educational Institutions
2. Choose payee as IIIT Kottayam
3. Choose payment category as VEGA Processor Workshop



February 2 & 3, 2024



**IIIT Kottayam, Valavoor P.O,
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Registration Form: <http://tinyurl.com/ydr87jb6>



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INFORMATION TECHNOLOGY

सत्यमेव जयते

More info on VEGA processor: www.vegaprocessors.in